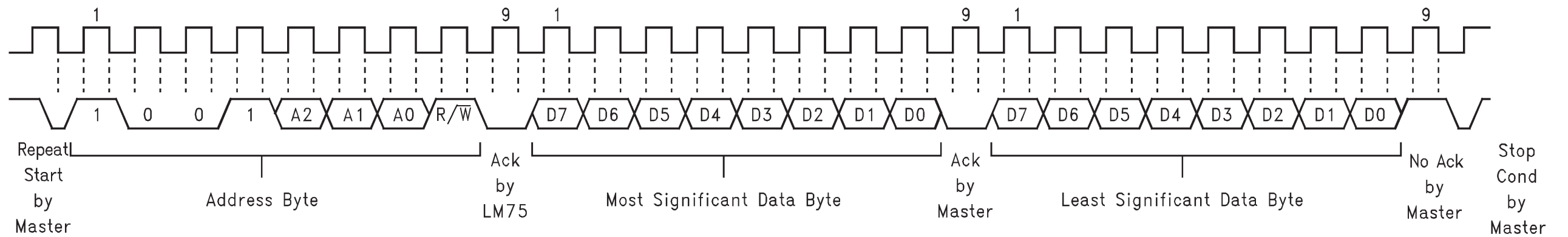
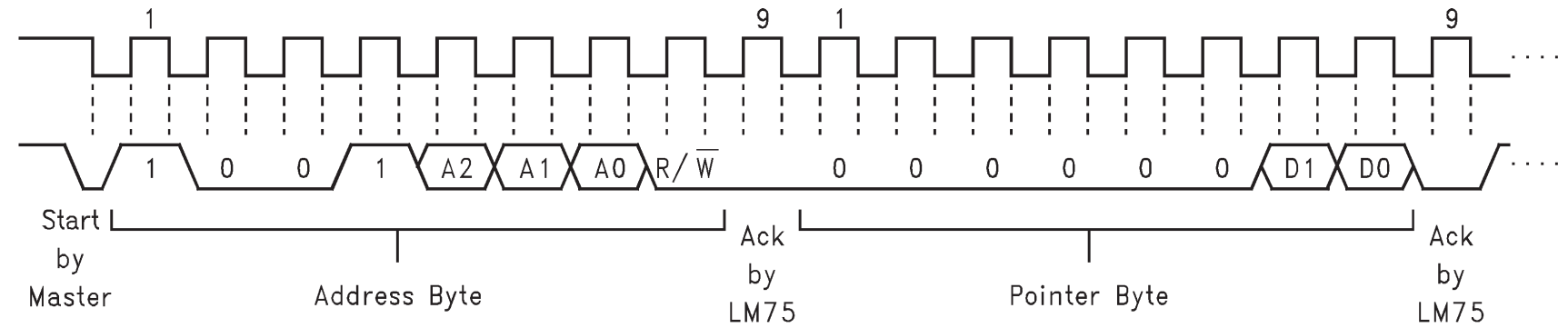
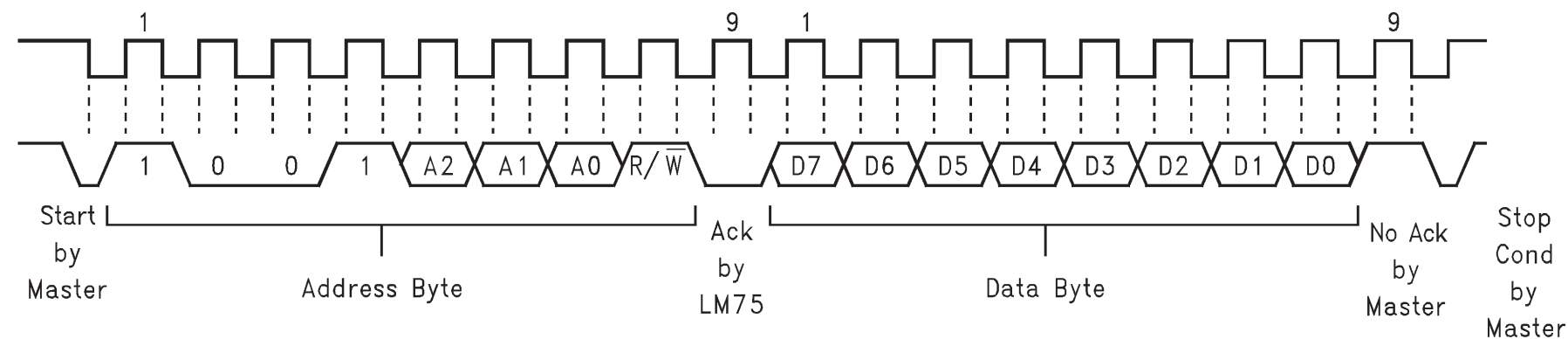


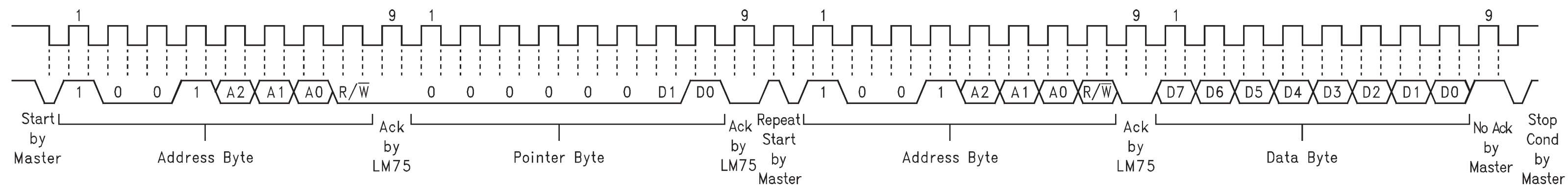
(a) Typical 2-Byte Read From Preset Pointer Location Such as Temp, T_{OS} , T_{HYST}



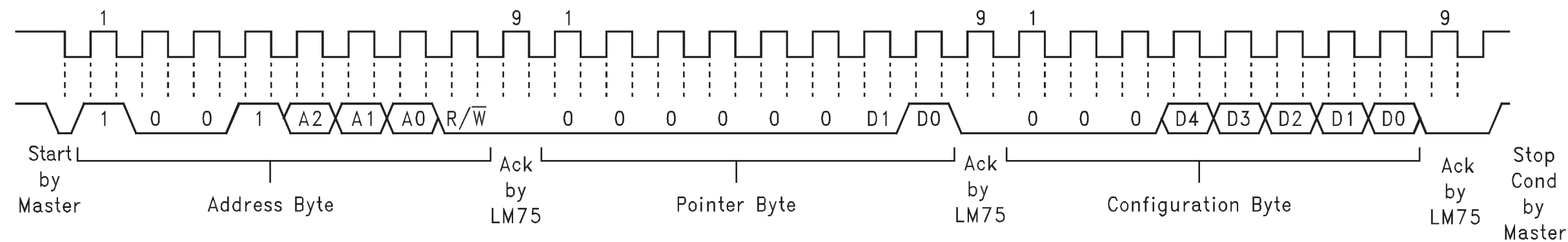
(b) Typical Pointer Set Followed by Immediate Read for 2-Byte Register such as Temp, T_{OS} , T_{HYST}



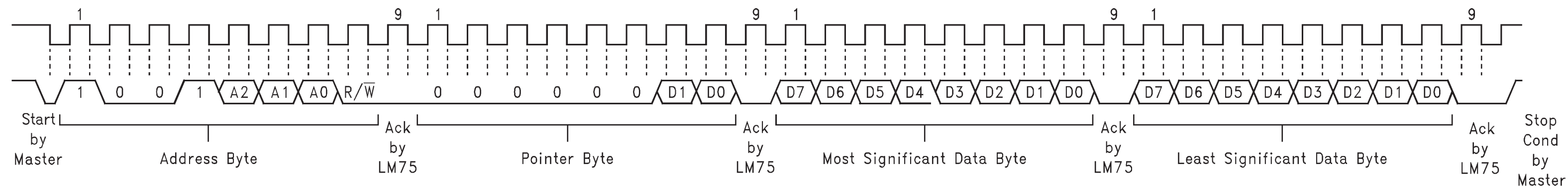
(c) Typical 1-Byte Read From Configuration Register With Preset Pointer



(a) Typical Pointer Set Followed by Immediate Read from Configuration Register



(b) Configuration Register Write



(c) T_{OS} and T_{HYST} Write